



Docket No. P15335

Application No.: 10/608,870

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

The Application for:

Mark T. Bohr, et al.

Serial No. 10/608,870

Filed: June 27, 2003

For: **PMOS TRANSISTOR STRAIN OPTIMIZATION
WITH RAISED JUNCTION REGIONS**

Examiner: Dao H. Nguyen

Art Unit: 2818

Confirmation No. 7488

Do Not Enter

Thanks,

DN

01/22/2007

RESPONSE TO OFFICE ACTION

Commissioner for Patents
BOX AF
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated October 31, 2006, please amend the above-captioned application as set forth below.

Amendments to the Claims are reflected in the listing of claims and begin on page 2 of this paper.

Remarks begin on page 6 of this paper.

I, Julie Dussault, hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on December 22, 2006.


(Signature of person mailing correspondence)